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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10071144	02/06/2002	287	412	2818	D. LE

\*\*APPLICANTS: Zheng Jun-Fei; Doyle Brian; Bai Gang; Liang Chunlin;

\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/451,696 11/30/1999

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no	042390P7480D
Verified and Acknowledged Examiner's initials			
TITLE : Work function tuning for MOSFET gate electrodes			

U.S. DEPT. OF COMM./PAT & TM-PTO 436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Draw.	Fig.s Draw.	Print Fig.
TERMINAL DISCLAIMER		Application Examiner		
<p><b>PREPARED FOR ISSUE</b></p> <p>WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent &amp; Trademark Office is restricted to authorized employees and contractors only.</p>				

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DISK (CRF)

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